

Electronic Patent Application Fee Transmittal

Application Number:	10758802			
Filing Date:	15-Jan-2004			
Title of Invention:	Methods of fabricating a semiconductor device having multi-gate insulation layers and semiconductor devices fabricated thereby			
First Named Inventor:	Dae-Woong Kang			
Filer:	Alexander Charles Johnson/Li Mei Vermilya			
Attorney Docket Number:	8750-042			
Filed as Large Entity				
Utility Filing Fees				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:				
Pages:				
Claims:				
Miscellaneous-Filing:				
Petition:				
Patent-Appeals-and-Interference:				
Post-Allowance-and-Post-Issuance:				
Extension-of-Time:				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	790	790
Total in USD (\$)				790